



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE COMMISSIONER OF PATENTS

Appl. No. : 10/790,747
Applicant : J. Lauffer et al
Filed : 03/03/04
Art Unit :
Examiner :
Docket No. : EI-2-04-002
Title : Circuitized Substrate With Signal Wire Shielding, Electrical
Assembly Utilizing Same and Method of Making
Assignee : Endicott Interconnect Technologies, Inc.

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

ERROR IN PRINTED PUBLICATION

Applicants note a serious error with respect to the published patent application
US 2005/0195585 A1, which was supposedly published from their Application Serial No.
10/790,747 (above). A copy of the publication is included.

Specifically, the published drawings are not those filed in Applicants' application. A
copy of said drawings is also included.

Please correct this error as soon as possible.

Respectfully submitted,

By

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EI-2-04-002



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I hereby certify that this correspondence is being deposited with United States Postal Service in an envelope addressed to:

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On February 6, 2006.

Signature

Christine Lang
Typed or printed name of person of signing Certificate

EI-2-04-002 – Error In Printed Publication Letter, Application Publication
US 2005/0195585 A1, EI-2-04-002 filed formal drawings, Postcard.

Note: Each paper must have its own certificate of mailing, or this certificate must identify each submitted paper.